## **Integral University, Lucknow**

Makeup Test- 2012-2013(Odd Semesters)

## NANOELECTRONICS (EC-707)

Year: 4 <sup>th</sup>	Sem: VII <sup>th</sup>	Groups: EC1 and EC2
Max. Marks: 15 Time: 1 hour 3		Time: 1 hour 30 minutes
Note: (1). At	tempt any 3(three) Questions.	
(2). Al	l questions carry equal marks.	
Q1. What is SET? And give its fabrication techniques.		(5)
Q2. Design circuits using RTD for ORing, ANDing and Inverter operations.		erter operations. (5)
Q3.What are	the limits of microminiaturization in silicon.	(5)
Q4. What are	e Carbon Nanotubes. What is the base material i	required for the
construction	of CNTs?	(5)
O5 What is RTBT? Explain the RTBT MOBILE and RTBT Threshold Gate?		